

**In the claims:**

*Please amend the claims as follows:*

1-8. (Canceled)

9. (Previously Presented) The domino logic circuit of claim 34, wherein the body bias voltage is a forward body bias voltage.

10-15. (Canceled)

16. (Previously Presented) A domino logic circuit comprising:

a pulldown circuit having a dynamic node;

a keeper connected to the pulldown circuit at the dynamic node; and

a source of a body bias voltage, the source of the body bias voltage being connected to the keeper to supply the body bias voltage to the keeper to bias the keeper;

wherein source supplies the body bias voltage such that the body bias voltage alternates between a first forward body bias voltage value and a second reverse body bias voltage value.

17. (Original) The domino logic circuit of claim 16, further comprising a foot transistor for connecting the pulldown circuit to ground.

18. (Original) The domino logic circuit of claim 16, wherein the pulldown circuit is connected to ground without an intervening foot transistor.

19-25. (Canceled)

26. (Currently Amended) ~~The domino logic circuit of claim 6,~~ A domino logic circuit comprising:

a pulldown circuit having a dynamic node;

a keeper connected to the pulldown circuit at the dynamic node; and

a source of a body bias voltage, the source of the body bias voltage being connected to the keeper to supply the body bias voltage to the keeper to bias the keeper;

wherein the body bias voltage is a reverse body bias voltage;

wherein the source supplies the reverse body bias voltage such that the reverse body bias voltage alternates between two values; and

wherein the source supplies the reverse body bias voltage such that the reverse body bias voltage alternates between the two values in accordance with a clock signal applied to the domino logic circuit.

27-33. (Canceled)

34. (Previously Presented) A domino logic circuit comprising:

a pulldown circuit having a dynamic node;

a keeper connected to the pulldown circuit at the dynamic node; and

a source of a body bias voltage, the source of the body bias voltage being connected to the keeper to supply the body bias voltage to the keeper to bias the keeper;

wherein the source supplies the body bias voltage such that the body bias voltage alternates between two values; and

wherein the source supplies the body bias voltage such that the body bias voltage alternates between the two values in accordance with a clock signal applied to the domino logic circuit.

35-41. (Canceled)

42. (Previously Presented) The domino logic circuit of claim 16, wherein the source supplies the body bias voltage such that the body bias voltage alternates

between the first forward body bias voltage and the second reverse body bias voltage in accordance with a clock signal applied to the domino logic circuit.

43-51. (Canceled)